

STK Bladestore Tests

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Overview

The STKB ladestore is a disk subsystem consisting of ATA disks, fiber channel connectivity, and a RAID controller (LSI manufactured). There are essentially four host connections * and four backend fiber connections. The host side ports are 2 Gb/sec and with their advertised 400 MB/sec bandwidth, the disk side ports are 1 GB/sec.

Our goal is to test this flavor of disk to see what the real world performance might be.

 $*Actually 8 ports a cross four cards, but they recommend to use only one portone \\basic ally mini-hubs$ a chear das those are

Hardware

TheborrowedSTKBladestoreconsistedof2"B150"diskdrawerseachwith10blades offive250GBdrivesforatotalrawcapacityofjustunder25TBintwodrawers,one "BC84"ControlModule,andthe"F40 "40Uheightcabinet.

In 16 Uofheightina 40 Ucabinet, there was 567lbs of hardware. That is something to make note of. A full rackwould weighin at 1147 pounds.

ThehostsystemisanIBM7026- 6M1with8GBofRAM,8PowerPC_RS64IV processors,and fourseparateRIOdrawers.TheHBAsareIBM6228fiberchannelcards whichare2gigabitpersecond(gb/s)speedcapable.Thediskswereconnectednatively tothe6M1withouttheuseofafiberswitch(suchasaBrocade).Thissystemhas sustainedI/Oth roughputsofover600MB/sec,soitwasmorethanadequateforthistest. Itisalsorepresentativeofwhatweruninourproductionstorageenvironment.

Theoperating system is AIX5.1 with maintenance level 3 applied.

Limitations

Connectednatively aswewere, there is an apparent two -pathlimitation from the host. This means that I would only be able to test two HBAs worth of through put. Although I haven't verified it, my suspicion is that this would be alleviated in a SAN environment perhaps a Brocade fibers witch in -between the disk subsystem and the host. IBM support verified this. Given more time, I would test that assumption.

Itshouldalsobenotedthatittakesquiteabitoftimetoreconfigure/formatnewRAID groupsintheBladestore,as thediskisquitedenseforitssize.Wefirstconfigured2TB LUNsandthattookover18hourstoformat.

Tests

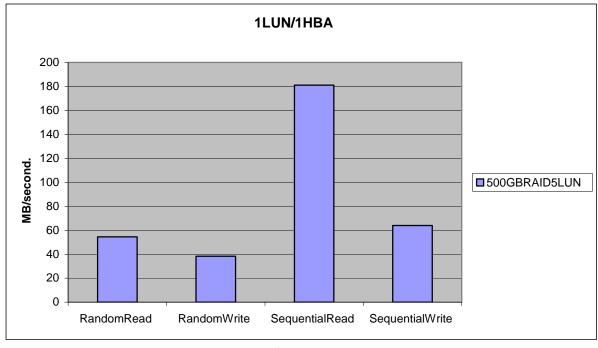
Allrunshave2typesoftestsrunagainstthem:randomaccessandsequential.The randomaccessteststakeaLUN,splitsitinto16l ogicalvolumes,andthenhammers away(readsorwritesdependingonwhichpartofthetestisbeingrun)with4MBbuffers atanypointinthelogicalvolumefor10minutes(5minutesforwritesthen5minutesfor reads).Itisknowas"donnie."

Theseq uentialtestissimplytheddcommandwith16MBbuffers.Writesaresimplya stringofzero's(/dev/zero).TheystartfromthebeginningoftheLUNand/orlogical volume(numbershaveproventobethesameeitherway)andareallowedtorunfora periodof timesufficienttogarnergoodnumbers.

TheHardFacts

Alltestswererunagainst 500 GBLUNs. This is the LUNsize that seemed to perform the best, was able to be recognized by AIX (2TBLUNsweren't able to be configured), and offeren ough size to be otentially useful without being too large for our intended purpose. The segment size on the controller was observed to be 128 KB.

ThefirstrunwasasingleLUNoverasinglepath.Onlyonehdiskisaccessed,making useofoneHBA.Asthereisnocontenti onbetweendisksorHBAs,thistest'sresultsare quiteusefulasabaselineandaidindeterminingwherebottleneckslayinfuturetests.

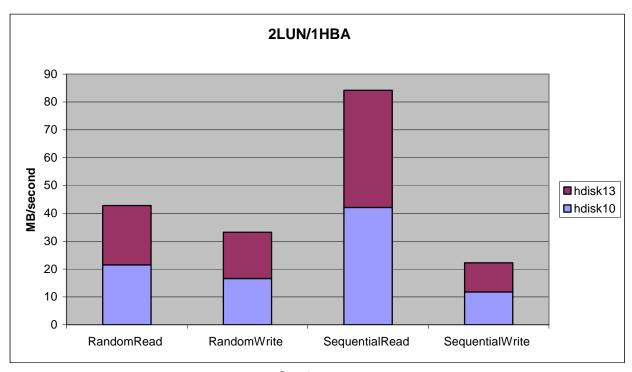


Run 1

Inthistestweseethattherandomreadsarearound54MB/secandrandomwrites are 38MB/sec.Sequentialreadsareimpressiveat181MB/sec,whichisnearthetheoretical limitof2GB/seclinespeeds.Sequentialwritesarelowat64MB/sec.Itwasobservedthat

the first few seconds (<=4) of a write test were approximately 50% fastert time in the remainder of that specific run. Is us pect this is a result of caching and is really not a true measure of sustained write performance.

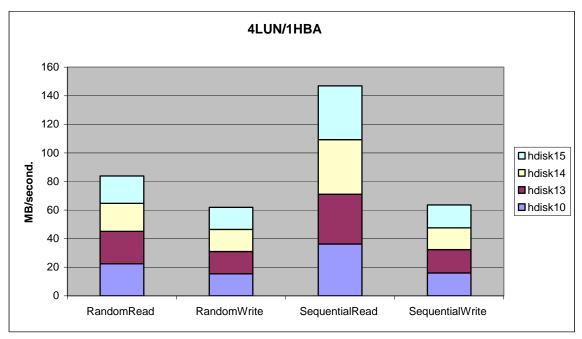
The second run is two LUNs over one HBA. Ideally we would expect to see at wo folding reach throughput for each test.



Run 2

We see that not only random reads dropper LUN, but the aggregate is even lower. This is also true for random writes, sequential reads, and sequential writes.

The thirdrunis four LUNs over rone HBA. Ideally, we would observe performance numbers be either a multiple of the number of LUNs over the baseline tests or benear the theoretical limit of 2Gb/sec fibers peeds (which is somewhere around 200 MB/sec), which ever is lower.

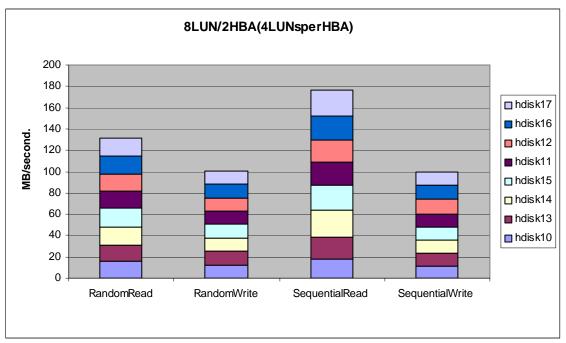


Run 3

Randomreadswereobservedtobeanaggregated83.8MB/sec.Thisisabouta62%drop fromwhatwemightexpecttoseeifitwerealinearimprovementwith4LUNs(4*54.4 or2Gb/secspeeds).Randomwritescameinatnearly62MB/ sec.Thisalsoshowsabout thesame60%dropinwhatmightbeexpectedlinearly.Atthispointwecanseethe caveataboutserialATAdiskinthenumbers:theyarenotpositionedtobehighI/Oper secondcapabledisksubsystems.Withroughlyfourtimesth enumberofI/Orequests comingintothecontroller,wesee60%lossofsingleLUNperformance(butstill achievingahigheraggregatednumber –unlikeRun2).

Sequentialreadsfelloffabitto147MB/secfromthesingletestof181MB/sec(a20% dropin overallperformance). AgainIsuspectthejugglingoffourrequeststreamshas somethingtodowiththis. Sequentialwritesappeartobeunaffectedat63.6MB/sec nearlythesameasasinglesequentialwrite. It appears the controller doesn't have a problem with I/O's when it's only being fedat64MB/sec.

The fourthrun is 8LUNs over 2HBAs. This test is useful because we can determine where a bottleneck is between the controller and host port cards on the controller (fiber runs).



Run 4

Randomreadsperformedatanaggregaterateof131MB/sec.Thisistheculmination of twoHBAsperformingat65.6and65.4MB/secrespectively.Weseeagainthereduction of expected performance of the previous test —a21% drop over the 1 66MB/secwe'd expect to see by doubling our multiple LUN/single HBA test. However, the aggregate 131MB/sec is the most we've seen for the random read test.

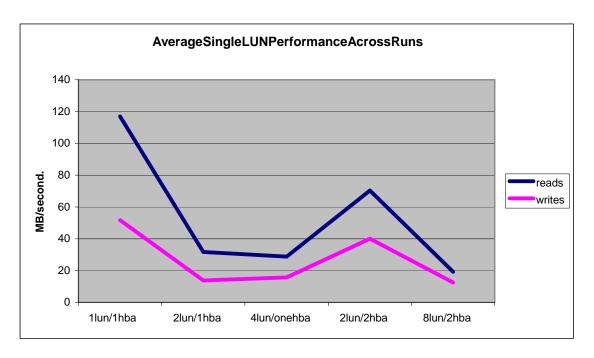
Randomwritesperformedat 100MB/sec. This is the culmination of two HBAs performing at 50.3MB / seceach. This also shows a similar 18% drop in expected performance we'd like to see over the multiple LUN/single HBA test of 124MB/sec (62 *2).

SequentialReadscamein176.75MB/sec.Thisisbetterthanthesequentialreadsofthe multipleLUN/s ingleHBAtest,butit'snearlythesame(aggregated)asthesingleLUN/singleHBAtest.Thiswouldseemtosuggestthatovertwohostports,thelimitis somewherearound180MB/seconreads.Thisisinlinewithmanufacturerclaimsof 400MB/sec,ifyou assumethatthetwounusedportswouldalsobeabletorunatthat speed.However,ifonehostportcanperformat146MB/seconmultipleLUNsequential reads,itseemslogicalthattwohostportsshouldhavebeenabletorunat292MB/sec (2x)sinceit's stillunderthe400MB/secclaimedthroughput.Sowemustnotassumethe abilityof400MB/secforanykindoftestfromthiscontroller/diskconfigurationcombo. Wewerelimitedbytheuseof2drawersofdisk,ratherthanthe4weinitiallyhopedfor. With4drawers,wewouldhavetheuseofmorebackendchannels.

SequentialWriteswere99.75MB/sec.Againweseethe21%dropinexpected performance(bydoublingtheprevioustest's63.6MB/sec).

Summation

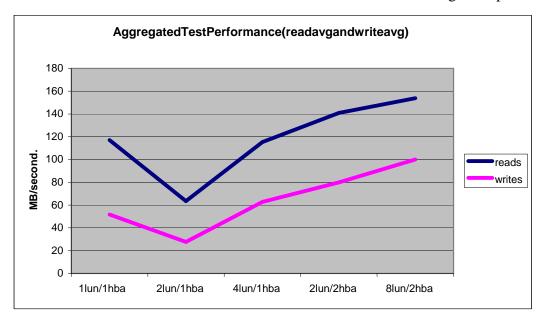
Let'stakealookataverageLUNperformance foreachofthetestsperformed.



Anadditionaltestwasrunforthischart –2LUNsover2HBAs. Youwouldexpecttosee a2ximprovementover1LUN/1HBA, againthisprovestobenotthecase.

Readsandwriteswereavera gedtogetherforsimplicity. Thatis, the sequential read values and random read values were averaged to gether to come up with one number for each test. It's apparent, as one studies this graph, that any kind of multiple requests on either reador write causes performance to drop. For use in HPSS, we might not want more than one requests treamper controller.

And finally, here is a chart for the aggregated performance for each run.



Againreadswereaveragedandwriteswerea veraged. This chart shows an upward trend as you add fiber channel cards. I'd like to see a four drawer Bladestore configuration utilizing all four backendand four front - end ports. Beyond what is represented here is speculation.

Otherlesstangiblethin gssuchasresistancetocorruption, ease of maintenance, availability, and others were not tested due to time constraints. Testing was limited to performance runsin order to determine a -world MB/second numbers.

Thepointtotakeawayfromthissuite oftestsisthatforanapplicationthatdoesnotneed morethanonestreamandperformssequentialI/O(withemphasisonreadingover writing),thisdisksubsystemmayrepresentsignificantbang -for-the-buck.Howeverin multiplestreamscenarioswherehi ghperformanceI/Onumbersareexpected(linespeed of 2Gb/sec, forexample),thissystemfallswayshortofthemark.